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PATENT
4450-0367P

IN THE U.S. PATENT AND TRADEMARK OFFICE

Applicant: GUERTIN, J. et al. Conf.: 1375
Appl. No.: 09/550,649 Group: 2633
Filed: April 17, 2000 Examiner: KIM, DAVID
For: METHOD OF TESTING BIT ERROR RATES FOR A
WAVELENGTH DIVISION MULTIPLEXED OPTICAL
COMMUNICATION

RECEIVED

JUN 26 2003

Technology Center 2600

LETTER TO THE OFFICIAL DRAFTSPERSON

June 24, 2003

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Attached hereto are three (3) sheets of corrected Formal Drawings. Please substitute these corrected drawings for the corresponding three (3) sheets of drawings on file in the above-identified application.

If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any


Appl. No. 09/550,649

overpayment to Deposit Account No. 02-2448 for any additional fee required under 37 C.F.R. §§ 1.16 or 1.17; particularly, extension of time fees.

Respectfully submitted,

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By


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4450-0367P

Attachment(s)

(Rev. 04/30/03)



Approved by DSK
25 August 2003

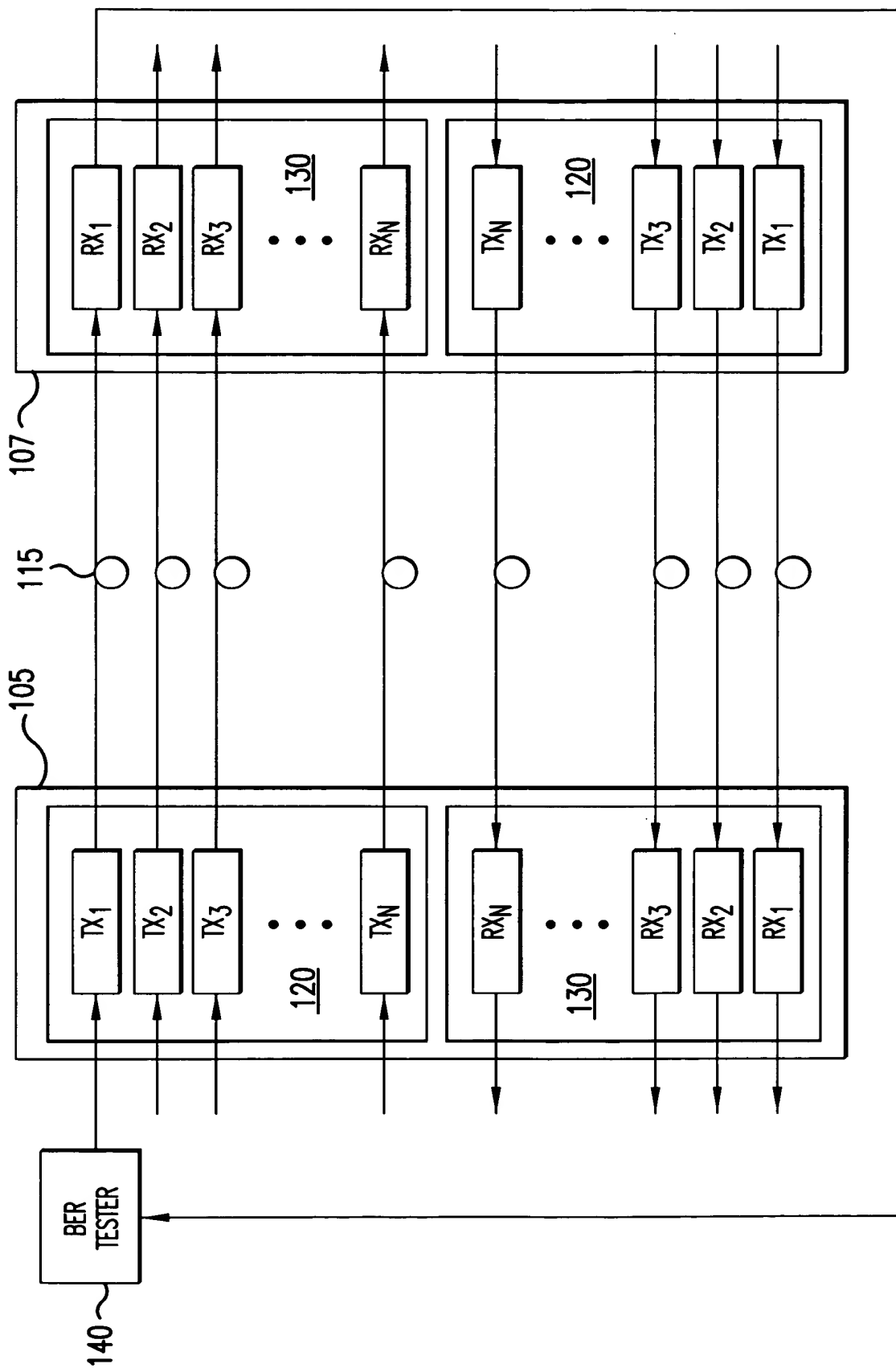


FIG.1

OI P R
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 25 August 2003

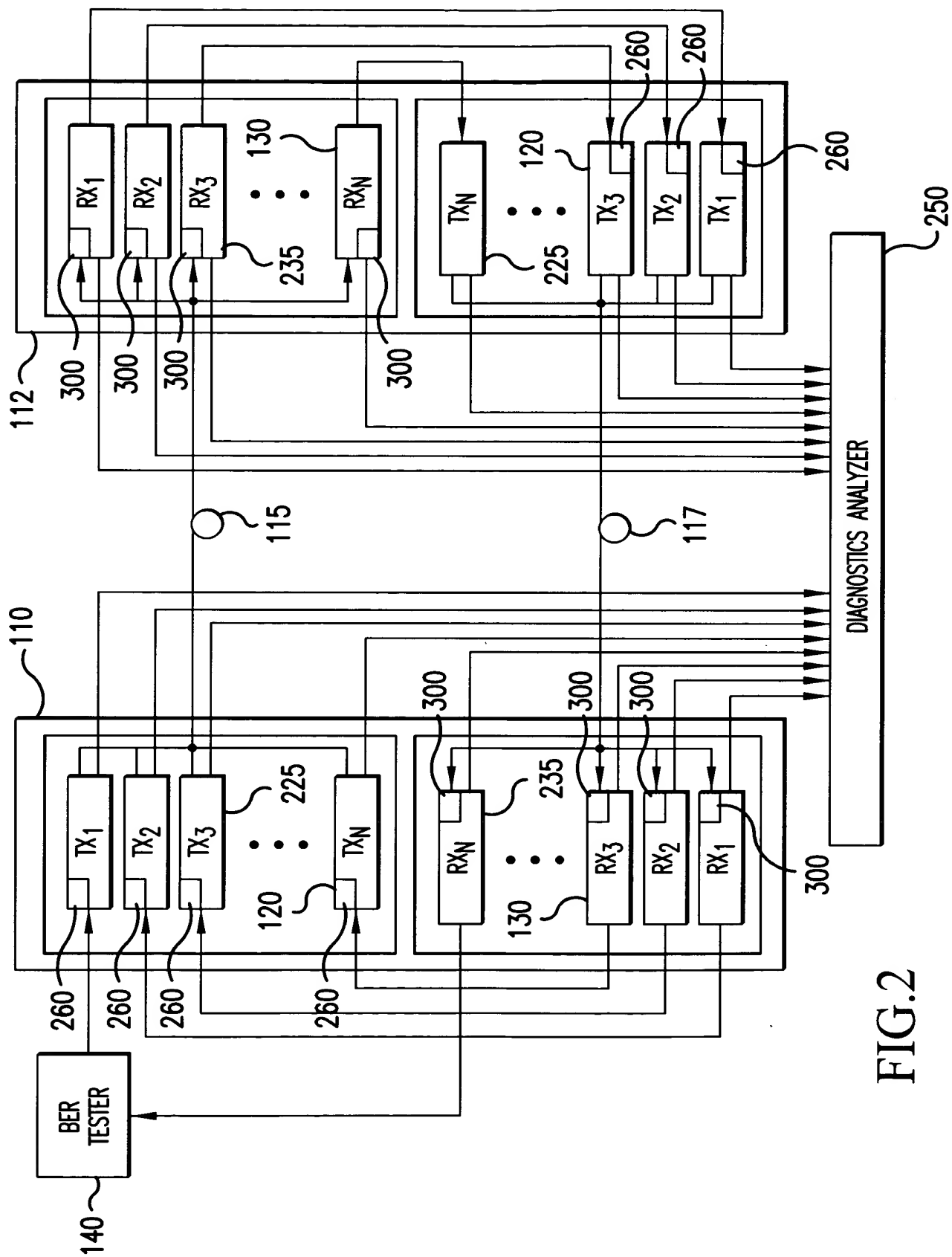


FIG.2



Approved by DSK
25 August 2003

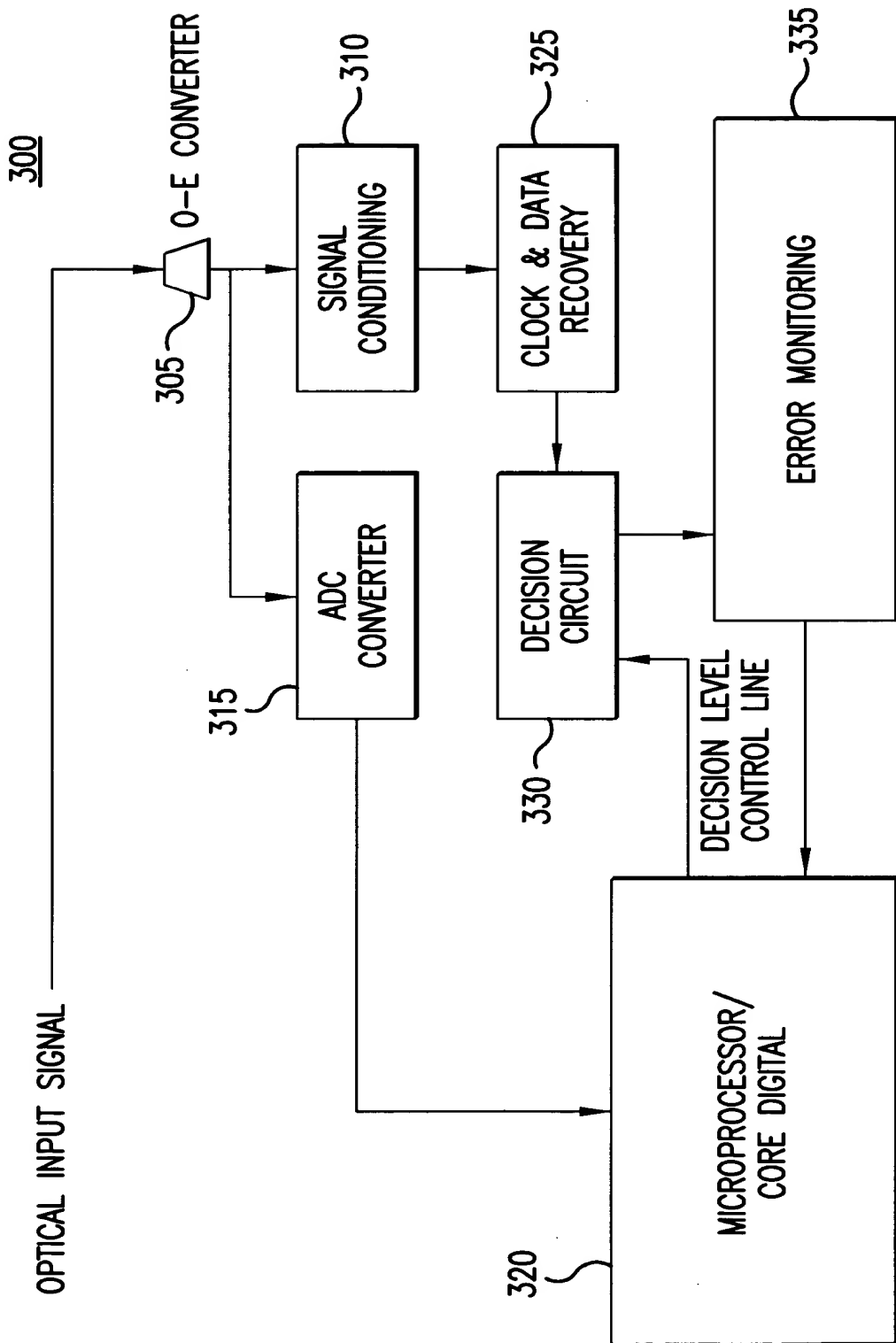


FIG.3